

FORM PTO-1449 (SUBSTITUTE)		Attorney Docket No.: P2001,0158	Applic. No. 10/657,928
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Applicant Wolfgang Gustin et al.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))		Filing Date September 10, 2003	Group Art Unit

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
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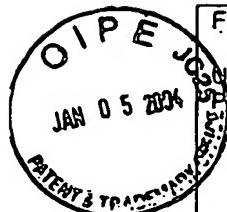
## FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
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	M						
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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>ZKS</i>	O	L. Nesbit et al.: "A 0.6 $\mu\text{m}^2$ 256Mb Trench DRAM Cell With Self-Aligned Buried STrap (BEST)", IEDM 1993, pp. 627-630
<i>ZKS</i>	P	G. Bronner et al.: "A Fully planarized 0.25 $\mu\text{m}$ CMOS Technology for 256Mbit DRAM and Beyond", 1995 Symposium on VLSI Technology Digest of technical Papers, Kyoto, Japan, pp. 15-16
EXAMINER <i>ZSDH</i>		DATE CONSIDERED 7/8/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;  
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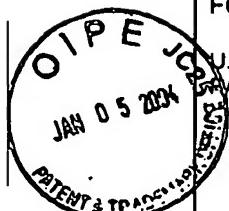
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<i>ZKS</i>	O	Stanley Wolf: "Silicon Processing For The VLSI Era – Volume 2: Process Integration", Lattice Press, Sunset Beach, California, cover page only
<i>ZKS</i>	P	D. Widmann et al.: "Technologie hochintegrierter Schaltungen", Springer Verlag, Heidelberg, Germany, 2 <sup>nd</sup> ed., cover page only

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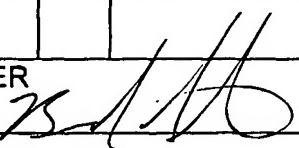
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES <input type="checkbox"/> NO <input checked="" type="checkbox"/>
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